

AMENDMENTS TO THE SPECIFICATION

On page 4, delete the second paragraph and replace with the following:

B¹

-- In accordance with the present invention, the load resistors in the RL - TAS latch, shown in Figure 2b, can be replaced with an active resistance load, for example, a trans-impedance amplifier stage (TIS). Figure 3a is a TAS - TIS latch in which the passive load resistance RL comprising R1, R2 has been replaced with an active TIS load including transistors T7', T8', T9, T10 T9, T10, T11, T12 and resistors R3, R4, R5, R6, R7, R8. The TIS load converts an input current to an output voltage. --